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Date *November 10, 2005* Client / Matter *100806-00264*
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Total number of pages, including cover letter: 3
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Comments

U.S. Patent Application No.: 10/827,067

SEMICONDUCTOR DEVICE

Our Reference No.: NEC 19.740A (100806-00264)

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COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

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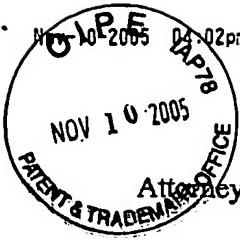
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T-426 P.004/005 F-093

Attorney Docket No.: NEC 19.740A (100806-00264)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor : Naoto Kimura
Title : SEMICONDUCTOR DEVICE
Serial No. : 10/827,067
Confirmation No. : 2361
Filed : 04/19/2004
Examiner : Luan C. Thai
Group Art Unit : 2891

Mail Stop: Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Comments on Statement of Reasons for Allowance

Sir:

The referenced application was allowed on August 25, 2005. The issue fee due on November 25, 2005 is being paid concurrently herewith.

Applicant hereby acknowledges the Examiner's Reasons for Allowance. Applicant respectfully notes that there may be additional reasons for allowance that have not been specifically cited, and which may apply to various of the allowed claims, in addition to or instead

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of the cited reasons. Applicant respectfully suggests that notwithstanding the Examiner's Reasons for Allowance, it is believed that each of the allowed claims is patentable in its own right and/or for other reasons raised during the prosecution and/or explained in the specification of this application.

To the extent that any statements regarding patentability of any claims allowed by the Examiner made by the Applicant or the Examiner in any document filed in this application are inconsistent with or not included in the Examiner's Reasons for Allowance, they are incorporated by reference herein.

Any fee due with this paper may be charged on Deposit Account No. 50-1290

Respectfully submitted,

Brian E. Hennessey
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